

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
MORIN ET AL.

Serial No. 10/701,165

Confirmation No. 5541

Filing Date: November 4, 2003

For: SEMICONDUCTOR DEVICE WITH MOS
TRANSISTORS WITH AN ETCH-STOP LAYER)
HAVING AN IMPROVED RESIDUAL STRESS)
LEVEL AND METHOD FOR FABRICATING)
SUCH A SEMICONDUCTOR DEVICE)

) Examiner: S. GEBREMARIAM

) Art Unit: 2811

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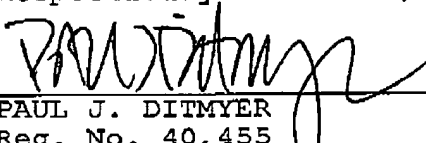
REQUEST FOR EXTENSION OF TIME

MS AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Sir:

The Applicants in the above-referenced application respectfully request a one-month time extension for filing the response to the Official Action mailed August 24, 2005. The Commissioner is hereby authorized to charge the extension fee in the amount of \$120.00 to the credit card noted in the attached credit card payment form. If any additional extension and/or fee is required, charge Account No. 01-0484.

Respectfully submitted,

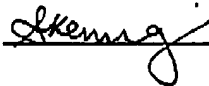


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CERTIFICATE OF FACSIMILE TRANSMISSION

I HEREBY CERTIFY that the foregoing correspondence has been forwarded via facsimile number 571-273-8300 to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 this 23rd day of December, 2005.

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